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R0642  T1 INCREMENT. IF SCALPREP DETECTS THIS INTERVAL THE T1,T2 AND SCALER
R0643  DATA ARE NOT COMPATABLE AND RETURN IS TO L+1 FOR ANOTHER READING OF THE
R0644  DATA. OTHERWISE, THE RETURN IS TO L+2 TO PROCEED. ROUTINE ALSO PREPARES
R0645  THE SCALER READING FOR COMPUTATION OF THE INCREMENT TO UPDATE T1T2. (THE
R0646  10 MS BIT (BIT 6) OF THE SCALER IS INCREMENTED 5 MS OUT OF PHASE FROM
R0647  T1.) ADDITION OF 5 MS (BIT 5) TO THE SCALER READING HAS THE EFFECT OF
R0648  ADJUSTING BIT 6 IN THE SCALER TO BE IN PHASE WITH BIT 1 OF T1. THE LOW 5
R0649  BITS OF THE SCALER READING ARE THEN SET TO ZERO, TO TRUNCATE THE SCALER
R0650  DATA TO 10 MS. RESULTS ARE STORED IN MPAC, +1.

 0651                          26,3714  0 0006 1  SCALPREP EXTEND
 0652  REF 688  LAST 1414     26,3715  22 156 0           QXCH   MPAC     +2
 0653  REF   3  LAST  424     26,3716  0 4527 0           TC     FINETIME +1
 0654                          26,3717  0 0003 1           RELINT
 0655  REF 689  LAST 1415     26,3720  52 155 1           DXCH   MPAC
 0656  REF  44  LAST 1396     26,3721  3 4706 1           CA     BIT5            ADD 5 MS TO THE SCALER READING.
 0657  REF 226  LAST 1402     26,3722  54 001 1           TS     L
 0658  REF 265  LAST 1409     26,3723  3 4714 1           CA     ZERO
 0659  REF 690  LAST 1415     26,3724  20 155 1           DAS    MPAC
 0660  REF   9  LAST  356     26,3725  4 4362 0           CS     LOW5            SET LOW 5 BITS OF (SCALER+5MS) TO ZERO
 0661  REF 691  LAST 1415     26,3726  7 0155 1           MASK   MPAC     +1      AND STORE RESULTS IN MPAC,+1.
 0662  REF 692  LAST 1415     26,3727  56 155 0           XCH    MPAC     +1
 0663  REF  10  LAST 1415     26,3730  7 4362 0           MASK   LOW5            TEST LOW 5 BITS OF SCALER FOR THE FIRST
A0664                                                                           INTERVAL AFTER THE T1 INCREMENT
A0665                                                                           (NOW = 00000, SINCE BIT 5 ADDED).
 0666  REF 389  LAST 1411     26,3731  10 000 0           CCS    A               IS IT 1ST INTERVAL AFTER T1 INCREMENT
 0667  REF 693  LAST 1415     26,3732  24 156 0           INCR   MPAC +2         NO
 0668  REF 694  LAST 1415     26,3733  0 0156 0           TC     MPAC +2         YES

R0669  POSTAND RECOVERS TIME AFTER STANDBY.THE SCALER IS SNAPSHOTTED AND THE
R0670  TIME1 TIME2 COUNTER IS SET TO ZERO. THE LOW 5 BITS OF THE SCALER ARE
R0671  INSPECTED TO INSURE COMPATABILITY BETWEEN THE SCALER READING AND THE
R0672  CLEARING OF THE TIME COUNTER. IT THEN COMPUTES THE DIFFERENCE IN SCALER
R0673  VALUES (IN DP) AND ADDS THIS TO THE PREVIOUSLY SNAPSHOTTED VALUES OF
R0674  TIME1 TIME2 AND PLACES THIS NEW TIME INTO THE TIME1 TIME2 COUNTER.

 0675  REF   1                                           COUNT* $$/P05

 0676  REF  38  LAST 1414     26,3734  4 4700 0  POSTAND CS     BIT11           RECOVER TIME AFTER STANDBY.
 0677                          26,3735  0 0006 1           EXTEND
 0678  REF  15  LAST 1414     26,3736  03 013 0           WAND   CHAN13          CLEAR STANDBY ENABLE BIT
 0679                          26,3737  0 0004 0           INHINT
 0680  REF 266  LAST 1415     26,3740  3 4714 1           CA     ZERO
 0681  REF 227  LAST 1415     26,3741  54 001 1           TS     L
 0682  REF  32  LAST 1414     26,3742  52 025 1           DXCH   TIME2           CLEAR TIME1TIME2
 0683  REF   2  LAST 1414     26,3743  0 3714 0           TC     SCALPREP        STORE SCALER IN MPAC, MPAC+1
 0684  REF   2  LAST 1414     26,3744  0 3737 1           TC     POSTAND +3      T1,T2,SCALER NOT COMPATIBLE
 0685                          26,3745  0 0006 1           EXTEND                 T1,T2 AND SCALER OK
 0686  REF   3  LAST 1414     26,3746  4 1153 0           DCS    SCALSAVE
 0687  REF 695  LAST 1415     26,3747  20 155 1           DAS    MPAC            FORM DP DIFFERENCE OF POSTSTANDBY SCALER
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